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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Serial No.: 10/565,006

Confirmation No.:

In re Application of:

Yasuyo SOGAWA, *et al.*

Group Art Unit:

Filed: January 18, 2006

For: SEMICONDUCTOR INTEGRATED CIRCUIT

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Customer Window
U.S. Patent & Trademark Office
Randolph Building
401 Dulany Street
Alexandria, VA 22314

Sir:

Further to the Information Disclosure Statement filed on April 18, 2006 wherein Japanese Patent Application No. 52-67741 was cited, filed herewith is a statement provided by applicants' Japanese patent attorney.

Respectfully Submitted,


STEPTOE & JOHNSON LLP

January 8, 2007

RWP/lmw

Attorney Docket: 28951.1169

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Applicant: Y. SOGAWA et al.

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Group Art Unit:

**INFORMATION DISCLOSURE STATEMENT
BY APPLICANT**

APR 18 2006

U.S. PATENT DOCUMENTS

Initials*	Document No.	Date MM/YYYY	Inventor	Class	Subclass	Filing Date If Appropriate
/LC/	5,719,449	02/1998	Strauss	257	786	
/LC/	2003/0032263	02/2003	Nagao et al.	438	462	

FOREIGN PATENT DOCUMENTS

Examiner's Initials*	Document No.	Date MM/YYYY	Country	Class	Subclass	Translation	
						Yes	No
/LC/	2001-93947	04/2002	Japan			Abstract	
/LC/	52-67741	06/1977	Japan				No
/LC/	7-37943	02/1995	Japan				No
/LC/	2002-22809	01/2002	Japan				No

OTHER DOCUMENTS

Examiner's Initials*	Include author, title of article, title of item (book, journal, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.

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05/11/2007

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